



U.S. UTILITY Patent Application

Q.I.P.E

PATENT DATE

APPLICATION NO. 09/975202	CONT/PRIOR	CLASS 327 710	SUBCLASS 305	ART UNIT 2816 2181	EXAMINER Binehart, Vu
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Knji Ishii

APPlicants

Method and apparatus for sharing signal pins on an interface between a system controller and peripheral integrated circuits

PTO-2040
12/99

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
	(Assistant Examiner)			Amount Due	Date Paid
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	(Primary Examiner)				
	(Legal Instruments Examiner)				

Frictional forces

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